

Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO. 219660US2S		SERIAL NO. NEW APPLICATION	
LIST OF REFERENCES CITED BY APPLICANT				APPLICANT Hisato OYAMATSU			
				FILING DATE Herewith		GROUP 2829	

0997 U.S. PTO  
10/075465  
02/15/02

U.S. PATENT DOCUMENTS							
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE	
/	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						

FOREIGN PATENT DOCUMENTS						
DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION			
			YES	NO		
AO						
AP						
AQ						
AR						
AS						
AT						
AU						
AV						

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)			
186	AW	Robert HANNON, et al., "0.25 <sup>m</sup> Merged Bulk DRAM and SOI Logic Using Patterned SOI", SYMPOSIUM ON VLSI TECHNOLOGY DIGEST OF TECHNICAL PAPERS, 2000 (2 pages)	
/	AX		
	AY		
	AZ		

Examiner ADD. S.

☐ Additional References sheet(s) attached

Date Considered 11/3/03

\*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.